


A diagram of a 4-bit shift register. It consists of four flip-flops arranged horizontally. The output of the first flip-flop is connected to the serial input of the second flip-flop, and so on. The serial input of the first flip-flop is labeled 'Serial Input'. The output of the fourth flip-flop is labeled 'Serial Output'.

100 101 102 103



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 1. The first row contains 20 empty boxes. The second row contains 19 empty boxes. The third row contains 18 empty boxes. The fourth row contains 17 empty boxes.



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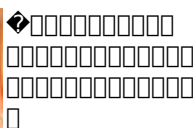


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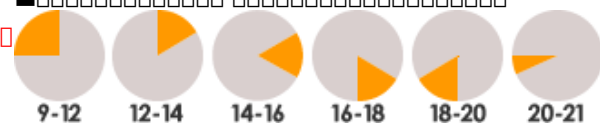


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